

JCG78 U.S. PTO  
09/915710  
09/27/01

103	Subclass
257	Class
ISSUE CLASSIFICATION	

PATENT NUMBER

**U.S. REISSUE Patent Application**

O.I.P.E. <i>[Signature]</i>	O.G. PUBLICATION DATE 10-18-01	REISSUE PATENT DATE
SCANNED <i>[Signature]</i> Q.A. <i>[Signature]</i>		

APPLICATION NO. 09/915710	CONT/PRIOR D F	CLASS 257	SUBCLASS 103	ART UNIT 2815	EXAMINER <i>[Signature]</i>
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APPLICANTS  
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Ako Hatano

TITLE  
Semiconductor device and method of fabricating the same

PTO-2040  
12/99

No

SURRENDER OF ORIGINAL PATENT

*[Signature]* (Ex. Initials)

ORIGINAL PATENT NUMBER 5,929,466

ISSUING CLASSIFICATION					
ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
257	103	257	96	97	90
INTERNATIONAL CLASSIFICATION					
H01L	33/00				
H01L	31/0304				
H01L	29/005				
<input type="checkbox"/> Continued on Issue Slip Inside File Jacket					

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets	Dwg.	Figs.	Dwg.	Print Fig.
	6	18	2		1
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) _____ (Date) _____			NOTICE OF ALLOWANCE MAILED	
<input checked="" type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. 5,654,832				ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	(Primary Examiner) _____ (Date) _____			Amount Due	Date Paid
FINAL SPRE REVIEW					
(INITIALS) _____	(Legal Instruments Examiner) _____			(Date) _____	

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